

Fig. 1

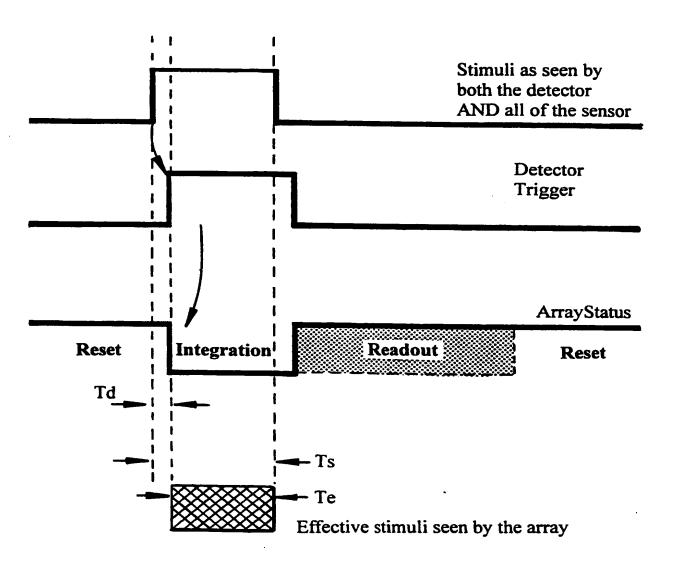


Fig. 2a

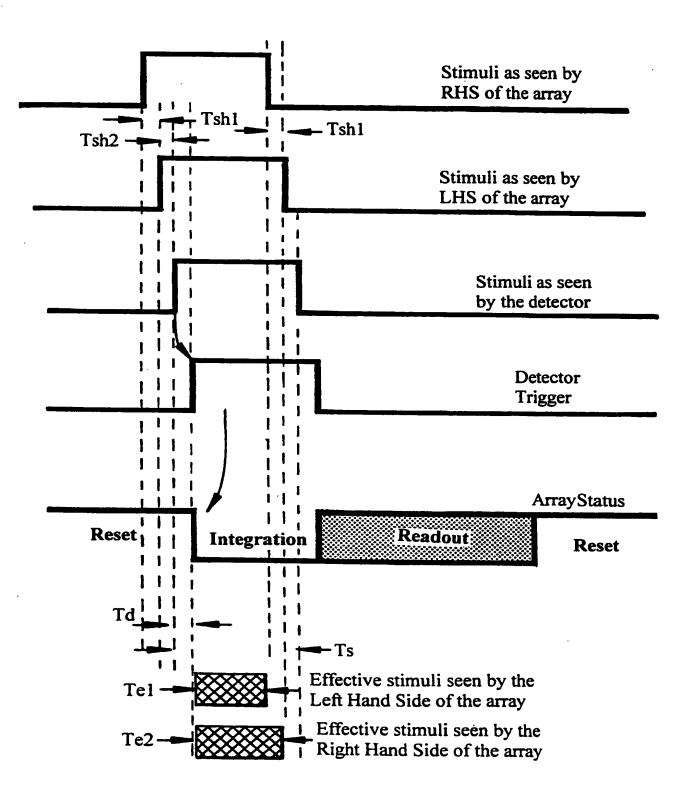
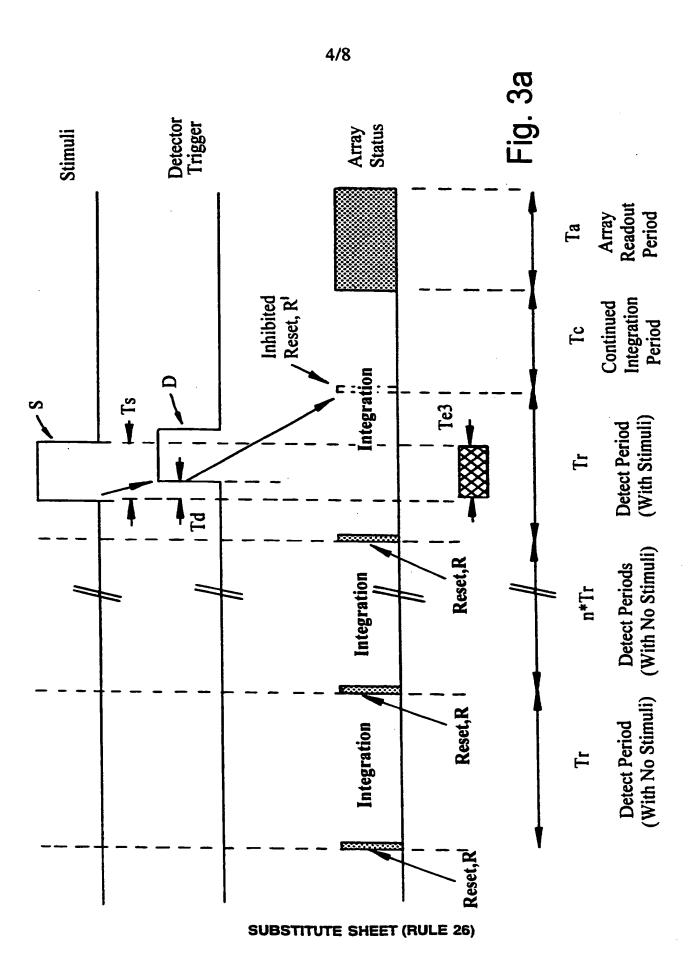
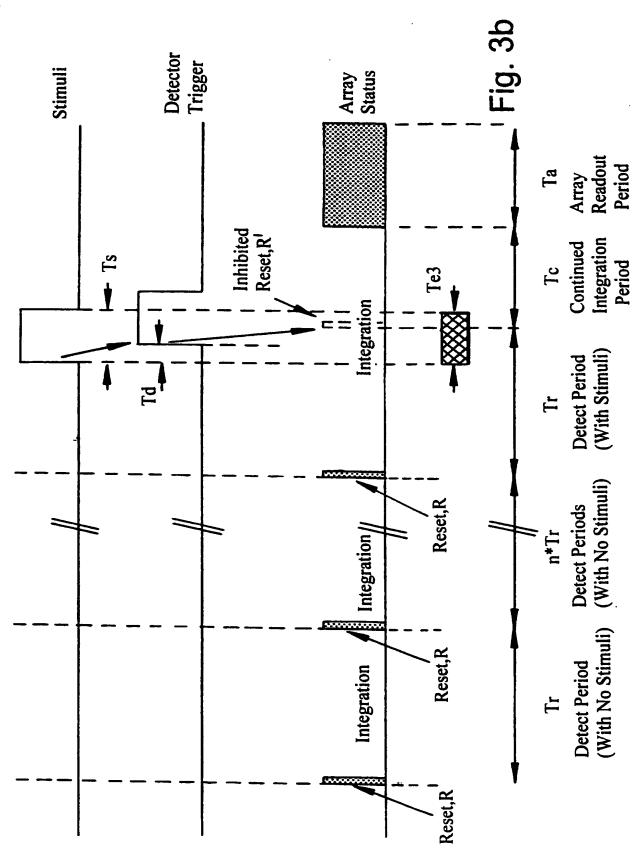
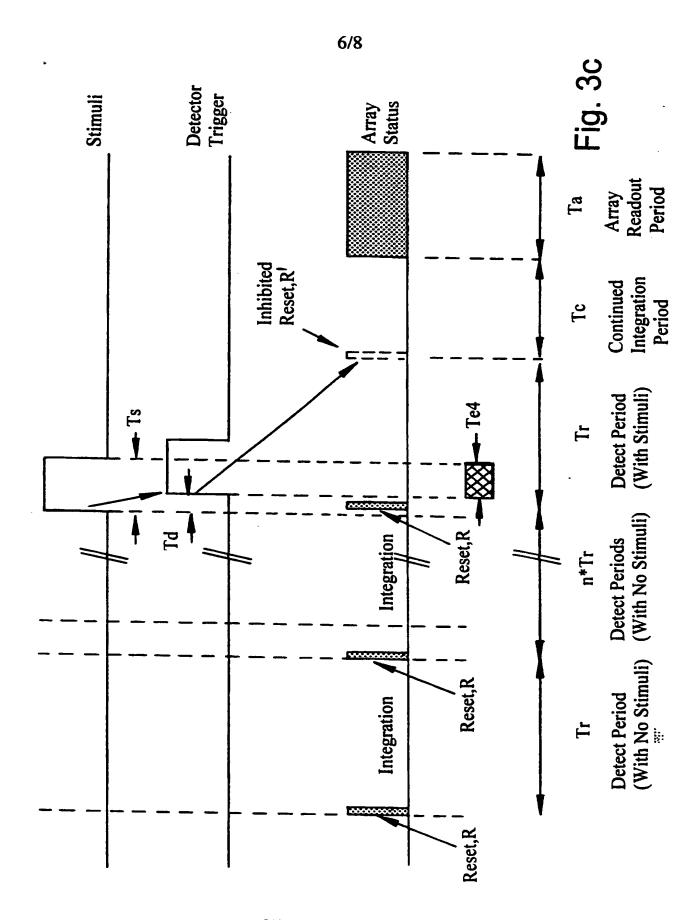


Fig. 2b





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Fig. 4 Stimuli Array Status Inhibited Reset,R' Ta Array Readout Period Integration Period I Integration Continued T Ts Te3 (With Stimuli) **Detect Period** Sub-array Integration Read (With No Stimuli) **Detect Periods** Reset,R Integration n\*Tr Sub-array Read Tsa Detect Period (With No Stimuli) Integration Reset, R Sub-array Read Reset,R 1

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